

Weekly Report

Date:9/10/13

Group name : Portable FPGA Based Serial Bus Multiplexer (Group 13)
Client/Advisor: Dr. Somani, Dr. Celik
Attendees/Role: Steven LeBlanc - Leader/Firmware Design Adriana Ceylan - Communications/Hardware Design Darin Cleveland - Webmaster/Hardware Design Justin Wheeler - Software Design

Accomplishments For Past Week

What	Who	Date
Locally installed Quartus, Qsys, etc., also went through tutorial of same and looked through USB documentation	Justin	9-10-13
Started reading Philips-ISP1362 documentation	Adriana	9-10-13
Researched Philips USB controller documentation	Steve	9-6-13
Got Quartus/Qsys project successfully compiled on laptop.	Darin	9-8-13

Plan for Coming Week/Individual Tasks

What	Who	When
Get control of USB controller through DMA	All	9/12
Implement Darin's UART module with the Nios II system	Adriana	9/14

Test out PIO with board to practice board communication.	Darin	9/17

Pending Issues

Issue	Responsibility
Work with CSG to resolve Quartus issue with Coover 2050 labs	Steve
Still need to test Quartus sample program with board. Serial cables have been ordered for future use.	Darin
Unable to access ISU website files server. Must solve.	Darin

Individual Hourly Contribution

Darin Cleveland	5
Adriana Ceylan	4
Steve LeBlanc	6
Justin Wheeler	4

-9/11 Meeting with Dr. Somani and Dr. Celik, minutes:

8:10 - Need to submit meeting minutes with the weekly report.

8:15 - List of tasks to get done before November 1st. Project must be done, presentation and poster preparation will take up a lot of time.

8:16 - How to test firmware section on its own? (solved at 8:54)

8:25 - Difficulties explaining design drawing of board. How does DMA work? Why is it in the current design?

8:39 - Polling data.

8:45 - We should not disrupt USB DMA if it's there. CPU writes to memory, USB sends info over.

8:50 - USB can be tested by the PC alone. Write to a buffer and read it.

8:54 - Can test firmware by using two Nios boards plugged into each other.