Weekly Report

Date:3/11/13

Group name: Portable FPGA Based Serial Bus Multiplexer (Group 13)

Client/Advisor: Dr. Somani, Dr. Celik

Attendees/Role:

Steven LeBlanc - Leader/Firmware Design

Adriana Ceylan - Communications/Hardware Design

Darin Cleveland - Webmaster/Hardware Design

Justin Wheeler - Software Design

Accomplishments For Past Week

What	Who	Date
Assembled UART code. Testbench still non-functional.	Darin	3/10
Created new design website via weebly. Htmls will be copied to the group 13 folder later. http://group13seniordesign.weebly.com/	Darin	3/8
Progressed in making a program to interface with a USB device	Justin	3/10
Got Eclipse working in the Digital Architecture Lab	Steve	3/7
Wrote a test program to interface simple switches and LEDs on the board through the Nios II processor. Worked on an unsuccessful program to print characters to the LCD screen via the Nios II processor	Steve	3/7
Worked on an unsuccessful program to print characters to the LCD screen via the Nios II processor	Adriana	3/7
Started working on the USB echo program, I ran into a licensing issue for interacting with the USB module architecture	Adriana	3/7

Plan for Coming Week/Individual Tasks

What Who When

Get UART testbench working. Debug. (current suspicion is that clock timing vs incoming information is not set at a proper ratio)	Darin	by 3/18
Transfer Weebly htmls to proper group folder.	Darin	by 3/12
Finish working out a program to open and modify a USB device file	Justin	by 3/12
Interface with the FPGA board with a C program	Justin	by 3/18
Work on UART Echo program on the Nios II	Steve	by 3/15
Work on USB Echo program on the Nios II	Adriana	by 3/15

Pending Issues

Issue	Responsibility
UART testbench not working.	Darin
I ran into a licensing issue for interacting with the USB module architecture.	Adriana
Need to verify "Hello World" with UART output	Steven and Adriana

Individual Hourly Contribution

Darin Cleveland	4
Adriana Ceylan	6
Steve LeBlanc	6
Justin Wheeler	2.5