

Weekly Report

Date:11/13/13

Group name : Portable FPGA Based Serial Bus Multiplexer (Group 13)
Client/Advisor: Dr. Somani, Dr. Celik
Attendees/Role: Steven LeBlanc - Leader/Firmware Design Adriana Ceylan - Communications/Hardware Design Darin Cleveland - Webmaster/Hardware Design Justin Wheeler - Software Design

Accomplishments For Past Week

What	Who	Date
Assisted Justin with testing API. Continued work on have a working uart-sram system.	Adriana	11-11
Worked on main.c file for USB controller communication. Changing commands and re-evaluating error printing. Unsuccessful.	Darin	11-10
Worked on 3 possible USB controller solutions (including the one with Darin)	Steve	11-12
Continued API testing, switched to dual-boot Ubuntu instead of an Ubuntu VM	Justin	11-13

Plan for Coming Week/Individual Tasks

What	Who	Date
Have a working system, use Darin's UART system in design. Sram problem might be a timing issue.	Adriana	11-16
Work on Poster and final presentation	All	11-22
Look at USB documentation for initialization commands.	Darin	11-22
Try to finish one of the USB implementations for	Steve	11-19

next week		
Get USB API testing/issues fixed.	Justin	11-22

Pending Issues

Issue	Responsibility
JTAG connections for programming the Nios Have become spotty. Connection will randomly die leaving us with inability to load new firmware.	Adriana, Darin, Steve
Quartus has become very touchy with removing hardware modules from the hardware solution. We need to find a solution that works, and slowly build off of it, saving copies of our system as we go along.	Adriana, Darin, Steve

Individual Hourly Contribution

Darin Cleveland	6
Adriana Ceylan	5
Steve LeBlanc	8
Justin Wheeler	6

Minutes:

8:12 - No headway. USB commands fixed as far as we can tell but still no results. Altera chip scope is software, we should use it.

8:15 - Found working USB VHDL code online. Take it and tack on our own code?

8:18 - VHDL to verilog translators are inaccurate: use to learn, not get functional code.

8:21 - Adriana can't get a working system with SRAM again. DE2 board has fickle timing constraints that interfere with SRAM compiling. Evidently, it only worked before because we thought SRAM was being used but it really wasn't.

8:27 - Justin trying to get Ubuntu VM working. Can't get his ports functioning. Need to instal linux on his computer instead of using Virtual Machine. Can put on a flashdrive and boot from it.

8:32 - Each of us should rank our progress on a success to failure scale.

8:34 - Darin working on main.c for USB, implementing fixes, still not responsive. Data printing problem? working on it. Need to initialize? Reset?

8:40 - Darin, succeeded with UART, failing everything worked on during semester 2.

8:43 - What relevant classes have we all had before senior design?

Presentation: point out where we got stuck and what we learned from it. Somani wants to be at the presentation.