Weekly Report

Date:10/9/13

Group name: Portable FPGA Based Serial Bus Multiplexer (Group 13)

Client/Advisor: Dr. Somani, Dr. Celik

Attendees/Role:

Steven LeBlanc - Leader/Firmware Design

Adriana Ceylan - Communications/Hardware Design

Darin Cleveland - Webmaster/Hardware Design

Justin Wheeler - Software Design

Accomplishments For Past Week

| What | Who | Date |
|---|---------|------------|
| Research alternatives for memory, decided on SDRAM. | Adriana | 10-5 |
| Created a project file to incorporate adding the SDRAM. | Adriana | 10-9 |
| Assisted Steven working out USB communication. Investigated USB Device demo program on Altera CD. | Darin | 10-6 |
| Working on Writing to USB Controller's Register and Reading Back | Steve | Unfinished |
| Working on getting the UART testbench to work. | Justin | 10-9 |

Plan for Coming Week/Individual Tasks

| What | Who | When |
|--|---------|-------|
| Incorporate the SDRAM into Jtag/USB/UART - UART- Nios system design. Start testing out the Nios system to test UART. | Adriana | 10-17 |
| Assist Steven with USB communication. | Darin | 10-17 |

| Rework Hardware project and implement "Golden code" | Steve | 10-11 |
|---|--------|-------|
| Work on writing to USB controller register and reading right back | Steve | 10-16 |
| Finish UART testbench one way or another, figure out how to test PC-side API. | Justin | 10-16 |

Pending Issues

| Issue | Responsibility |
|--|-----------------------|
| JTAG connections for programming the Nios Have become spotty. Connection will randomly die leaving us with inability to load new firmware. | Adriana, Darin, Steve |
| | |
| | |

Individual Hourly Contribution

| Darin Cleveland | 4 |
|-----------------|----|
| Adriana Ceylan | 4 |
| Steve LeBlanc | 11 |
| Justin Wheeler | 4 |

Meeting Minutes: 10-9

8:10 - THINGS ARE NOT LOOKING GOOD. 3 WEEKS BEHIND, WORK BOTTLENECKED.

- -USB controller write to register not working. As if code is not running.
- 8:22 debugging brainstorming.
 - -tutorials can be wrong. Weak pull up for unused pins causing problems?
 - -difference between on compiler and on embedded systems.
 - -positive and negative acknowledgments for debugging.
- 8:36 check all pins, see if any going to the USB. could be disabling it.
 - -Koray will work with Steven a bit, try to debug.

8:44 - must catch up.

8:50 - demo files not working. Could be due to outdated file paths. Recopy files to new directory.