Weekly Report

Date:10/30/13

Group name : Portable FPGA Based Serial Bus Multiplexer (Group 13)
Client/Advisor: Dr. Somani, Dr. Celik
Attendees/Role: Steven LeBlanc - Leader/Firmware Design Adriana Ceylan - Communications/Hardware Design Darin Cleveland - Webmaster/Hardware Design Justin Wheeler - Software Design

Accomplishments For Past Week

What	Who	Date
Help debug/code walk-through Steve's code on how to interact with the USB controller.	Adriana, Steve	10-23
Echo text from the board to the PC using the UART system (RS232 port). This test will prove that communication through Darin's UART component is working with the system.	Adriana	10-27

Plan for Coming Week/Individual Tasks

What	Who	When
Finish up implementation and test of UART to Nios system	Adriana	11-6

Pending Issues

Issue	Responsibility
JTAG connections for programming the Nios Have become spotty. Connection will randomly die leaving us with inability to load new firmware.	Adriana, Darin, Steve
Quartus has become very touchy with removing hardware modules from the hardware solution. We need to find a solution that works, and slowly build off of it, saving copies of our system as we go along.	Adriana, Darin, Steve

Individual Hourly Contribution

Darin Cleveland	
Adriana Ceylan	6
Steve LeBlanc	
Justin Wheeler	

Minutes: