

Weekly Report

Date:10/3/13

Group name : Portable FPGA Based Serial Bus Multiplexer (Group 13)
Client/Advisor: Dr. Somani, Dr. Celik
Attendees/Role: Steven LeBlanc - Leader/Firmware Design Adriana Ceylan - Communications/Hardware Design Darin Cleveland - Webmaster/Hardware Design Justin Wheeler - Software Design

Accomplishments For Past Week

What	Who	Date
Got PIO in and out to a functional state on laptop and board. Assisted Steven with further PIO and USB work.	Darin	9-29
No measurable progress this week	Steve	10-3
Jtag-UART testing. Dealing with on-chip memory problems.	Adriana	10-3
Refactored some existing code. Wrote testbench for UART.	Justin	10-3

Plan for Coming Week/Individual Tasks

What	Who	When
Get program to successfully poll information from the USB controller.	Darin	10-7
Get read/write to USB controller up and running	Steve	10-9
Test/Finish testing Jtag-UART echo	Adriana	10-9
Run testbench on UART code	Justin	10-9

Pending Issues

Issue	Responsibility
JTAG connections for programming the Nios Have become spotty. Connection will randomly die leaving us with inability to load new firmware.	Adriana, Darin, Steve

Individual Hourly Contribution

Darin Cleveland	8
Adriana Ceylan	5
Steve LeBlanc	4
Justin Wheeler	6

Meeting Minutes: 10/3

8:14 - Look over diagrams everyone made of the system: compare notes and understanding of system.

8:27 - Where we are on project:

- Adriana, trying to integrate UART onto FPGA, memory issues, use DRAM.
- Steven, trying to write and read to USB controller. Scheduling difficulties.
- Darin, got PIO read/write working on his laptop. Goal: same with USB. Having troubles understanding USB documentation.
- Justin, commenting API code, simulating/testing UART.

8:40 - hours worked.

8:44 - task list

- to simulate the robot, can use a second Nios board with the same program.