

Weekly Report

Date:4/1/13

Group name : Portable FPGA Based Serial Bus Multiplexer (Group 13)
Client/Advisor: Dr. Somani, Dr. Celik
Attendees/Role: Steven LeBlanc - Leader/Firmware Design Adriana Ceylan - Communications/Hardware Design Darin Cleveland - Webmaster/Hardware Design Justin Wheeler - Software Design

Accomplishments For Past Week

What	Who	Date
Worked on custom LCD module to practice integrating modules into Nios II/Avalon Memory Mapped Fabric	Steve	3/31/13
Determined that a special custom library (libusb) was required in order to pass data through USB, downloaded most recent stable version	Justin	3/31/13
Researched how libusb worked and how to use it	Justin	3/31/13
Performed monkey tests on UART	Darin	3/27/13
Met with Steve Nystrom (CSG) in the lab to demonstrate issues I have been having with licensing issue	Adriana	3/25/13

Plan for Coming Week/Individual Tasks

What	Who	When
Meet with Koray to put DE2 board under	Steve, Koray	Week of 4/1

O-scope and/or protocol analyzer		
Use new libusb library to send data to the FPGA board	Justin	4/8/13
Contact Koray to get info on how to interface our software with his program	Justin	4/8/13
Perform tests on UART with simulated noise/errors.	Darin	4/8/13
Since I can not get the library to work with USB serial port, I will try to create my own structure and communicate with the physical port	Adriana	4/9/13

Pending Issues

Issue	Responsibility
High Level System Architecture Diagrams and Explicit Designs	Steve
Licensing issues with Quartus II Avalon_SLS library (used to get the USB architecture)	Adriana

Individual Hourly Contribution

Darin Cleveland	1
Adriana Ceylan	1.5
Steve LeBlanc	5
Justin Wheeler	4

