

Weekly Report

Date:3/25/13

Group name : Portable FPGA Based Serial Bus Multiplexer (Group 13)
Client/Advisor: Dr. Somani, Dr. Celik
Attendees/Role: Steven LeBlanc - Leader/Firmware Design Adriana Ceylan - Communications/Hardware Design Darin Cleveland - Webmaster/Hardware Design Justin Wheeler - Software Design

Accomplishments For Past Week

What	Who	Date
Created program that takes in motor number, direction, and speed, and converts that into the sequence of hex instructions to be sent to the FPGA	Justin	3/14/13
Started High Level Block Diagrams for System Architecture	Steve	3/14/13
Got full UART to a functional state. UART is successfully transmitting and receiving data as intended.	Darin	3/24/13
Still trying to debug why USB architecture will not connect to Nios II processor.	Adriana	3/14/13
Wrote the design document rough draft	Adriana	3/14/13

Plan for Coming Week/Individual Tasks

What	Who	When
Start passing data to the FPGA via a C program	Justin	4/1/13
Finish System Architecture	Steve	3/29/13
Implement UART Echo program	Steve	3/29/13
Perform organized and monkey tests on UART.	Darin	to continue for the next couple weeks
Meeting with CSG to fix the licensing problem for the USB architecture	Adriana	as soon as possible - 3/30
Work on echo program for USB architecture	Adriana	as soon as possible - issue depending license

Pending Issues

Issue	Responsibility
High Level System Architecture Diagrams and Explicit Designs	Steve
Meeting with CSG to fix the licensing problem for the USB architecture	Adriana

Individual Hourly Contribution

Darin Cleveland	5
Adriana Ceylan	5
Steve LeBlanc	2

Justin Wheeler	3
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