

Weekly Report

Date:1/27/13

Group name : Portable FPGA Based Serial Bus Multiplexer (Group 13)
Client/Advisor: Dr. Somani, Dr. Celik
Attendees/Role: Adriana Ceylan, Justin Wheeler, Darin Cleveland, Steven Leblanc

Accomplishments For Past Week

What	Who	Date
Met with Dr. Somani for the first time	Darin, Adriana, Steve, Justin, Koray, Dr. Somani	1-31-13
Discussed board options and chose to use the Altera Cyclone II FPGA for this project. These are the boards that are in the 281 lab in Coover that we all learned to use in Digital Logic.	Darin, Adriana, Steve, Justin	1-29-13
Divided up general sections of the project. Darin and Adriana to work on the UART. Steve to work on the peripheral processor and code. Justin to work on Linux drivers and C++ API.	Darin, Adriana, Steve, Justin	1-31-13
Did general research on Altera's Nios II Soft Processor	Steve	2-3-13
Requested more information on the C++ API and any code that may have already been written by client, in order to get a better idea of how to interface the C driver with the C++ API.	Justin	2-3-13
General research on project and UART structure.	Adriana	1-30-13

Plan for Coming Week/Individual Tasks

What	Who	When
Schedule meeting to talk about timeline of project	Darin, Adriana, Steve, Justin	
Clarify relationships within Altera's product families about Nios II, Cyclone II, MIPS architecture	Steve, Koray	2-5-13
Further research for structure/design of UART	Adriana, Darin	

Pending Issues

Issue	Responsibility
Get Ubuntu Linux v11 Virtual Box or Dual Boot for Laptop	All
Research UART, MIPS architecture, USB, Linux Drivers	All
Timeline for project	All

Individual Hourly Contribution

Darin Cleveland	1
Adriana Ceylan	3
Steve LeBlanc	2
Justin Wheeler	1