

Weekly Report

Date:2/25/13

Group name : Portable FPGA Based Serial Bus Multiplexer (Group 13)
Client/Advisor: Dr. Somani, Dr. Celik
Attendees/Role: Steven LeBlanc - Leader/Firmware Design Adriana Ceylan - Communications/Hardware Design Darin Cleveland - Webmaster/Hardware Design Justin Wheeler - Software Design

Accomplishments For Past Week

What	Who	Date
Tested UART receiver module (functional) and created FIFO buffer module and testbench (not yet functional)	Darin	2/24
Worked on Tools Used, Reason For Tools, System Block diagram, Timeline/task breakdown on the Project Plan.	Adriana	2/21
Researched how Linux interfaces with USB and how to write programs that manipulate USB ports and devices, obtained API requirements from client	Justin	2/23
Still working on figuring out what constitutes a "basic" processor	Steve	2/22

Plan for Coming Week/Individual Tasks

What	Who	When
Test FIFO / debug. Combine FIFO and receiver and test.	Darin	By 3/3
Assist Darin with FIFO and receiver debugging and testing.	Adriana	By 3/3
Write up program to control USB port/device.	Justin	By 3/3
Continue working on the Nios II processor.	Steve	By 3/3

Pending Issues

Issue	Responsibility
Still working on loading a basic Nios II processor. Having difficulty with the "wizard" program that builds them.	Steve

Individual Hourly Contribution

Darin Cleveland	5
Adriana Ceylan	3
Steve LeBlanc	4
Justin Wheeler	3.5