**Dec12-04 Ultrasound Brain Imaging System**

**Weekly Report - 9 (Mar 20 – Mar 26)**

Group Members: Justin Batcheler, Jon Driggs, Francis Ferrer, Allen Kellar, Richard Page, Amairani Tapia

Faculty Advisor and Client: Dr. Timothy Bigelow

**Accomplishments of the last week**

* Working on getting running simulations for Cadence models found.
* Finished 1st draft of the design document.
* Started to revise project plan for next week.

**Plan for coming week**

* After talking to Dr. Bigelow we have decided to change from using A/D and D/A cards to using DSP chips and programming them to generate square waves.
* After we understand the DSP test board, we should be able to copy the circuit on that board and make our own DSP waveform generation circuit cards.

**Pending issues**

* Finding a company that can supply us with a transducer.

**Individual contributions – identify specific tasks accomplished**

* Richard:
  + Working on simulating the low-noise amplifier.
* Amairani and Allen:
  + Working on simulation of ADC and DAC.
* Justin and Francis:
  + Worked on designing and simulating the T/R switch.
* Jon:
  + Working on designing switching circuit for the H-bridge. Working on finding a DSP evaluation board to order for next week.

**Meetings**

* Group meeting on Wednesday with everyone in attendance.
* Group meeting on Thursday with everyone in attendance.
* Group meeting with Dr. Bigelow on Friday with everyone in attendance.

**Hours**

* Below are the hours worked for each member.

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
|  | Justin | Jon | Francis | Allen | Richard | Amairani |
| Last Week | 9 | 12 | 9 | 10 | 9 | 9 |
| Cumulative | 60 | 70 | 60 | 60 | 60 | 60 |